

ABSTRACT OF THE DISCLOSURE

An information processing apparatus includes a high-speed processor, and the high-speed processor processes a game program restored in a memory cartridge. A power control routine is then executed, and capacitors (C4, C5) included in a charge pump circuit is repeatedly charged and discharged. If an error occurs in the high-speed processor and then the power control routine is not properly executed, a difference in electric potential (V_C) between one end of a resistor (R10) and a reference electric potential surface or point increases. When the difference in electric potential exceeds a threshold value, a supply of stabilized voltage is stopped by a power on/off control circuit, and an entire system including the high-speed processor is then turned off.